

09/164,216  
Response to  
(Office Action Dated June 5, 2001)



#231D  
9-18-01  
R. Shaker  
PATENT  
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SEP 13 2001

TC 2800 MAIL ROOM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Ronald Pasqualini

Appln. No.: 09/164,216

Filed: September 30, 1998

For: ESD PROTECTION CIRCUIT UTILIZING  
FLOATING LATERAL CLAMP DIODES

Group Art Unit: 2811

Examiner: O. Nadav

RESPONSE (TO OFFICE ACTION DATED  
JUNE 5, 2001)

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the  
United States Postal Service, postage prepaid, in an envelope,  
addressed to Box ~~Non-Fee~~ Arr Commissioner for Patents,

Commissioner for Patents  
Washington, D.C. 20231

Washington D.C. 20231-9999 on Sept. 4, 2001  
Dated: 09-04-01 By: Robert D. King

Dear Sir:

In response to the Official Action mailed June 5, 2001, please amend the above-  
identified application as follows:

In the Claims

Claims 1, 5, 6, 10, 11, 17, 18, 20-23, and 33-37 have been cancelled. *N.E.*

Claim 15 has been amended to read as follows:

*Sub E11*  
*D1*  
15. (Twice Amended) A semiconductor chip having a substrate of a first  
conductivity type, the chip comprising:  
a plurality of pads;  
an electrostatic discharge (ESD) negative ring;  
a plurality of ESD positive lines, the plurality of positive lines not being connected to  
a steady voltage source, the plurality of positive lines not being electrically connected to each  
other;  
a plurality of ESD switches connected to the ESD positive lines and the ESD negative  
ring so that each positive line is connected to the negative ring via an ESD switch;